



Lokmanya Tilak Jankalyan Shikshan Sanstha's
PRIYADARSHINI COLLEGE OF ENGINEERING

(Recognised by A.I.C.T.E., New Delhi & Govt. of Maharashtra, Affiliated to R.T.M.Nagpur University)
Near CRPF Campus, Hingna Road, Nagpur-440 019, Maharashtra (India)
Phone : 07104 – 299681 Fax : 07104 – 237648
email : principal.pce.ngp@gmail.com, www.pcenagpur.edu.in



6.3.4:-[Number of teachers undergoing online/face-to-face Faculty development Programmes (FDP) during the year (Professional Development Programmes, Orientation / Induction Programmes, Refresher Course, Short Term Course etc.)

Sr. No	Name of teacher who attended	Title of the program	Duration (from – to) (DD-MM-YYYY)
1	Prof. H. K. Dubey	10 days Pre-PhD Course Work, organized by Academic Staff College, UGC Nagpur University, Nagpur	20/06/2017-30/06/2017
2	Ms.A.Kondelwar	One week course on "Advanced Wireless communication Technologies : A PHY-Layer Perspective" , VNIT, Nagpur	12 - 17 December 2016
3	Mrs. S.W.Varade	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
4	Mr.M.K.Demde	3 Days FDP on "Research & Teaching Methodologies" , SVP CET, Nagpur	17/11/2016 to 19/11/2016
5	Mr.V.K.Taksande	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
6	Mr.A.B.Jirapure	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
7	Mr.N.S.Ambatkar	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
8	Mr.V.G.Girhepunje	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017



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9	Mr.D.G.Gahane	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
10	Mr.V.P.Panchbhai	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
11	Ms.J.C.Kolte	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
12	Mrs. J.M.Bhattad	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
13	Mrs. S.P.Washimkar	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
14	Mr.P.P.Ashtankar	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
15	Ms. V.G.Nasre	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
16	Ms. S.G.Mungle	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
17	Ms.Nilima Bodhaye	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
18	Ms.Neha Soundhiya	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017



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19	Ms.Padmaja Ayachit	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
20	Ms.Shweta sarode	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
21	Ms.Vaishali Ghodichor	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
22	Ms.Ritupriya Bobhate	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
23	Mr.Vishal muley	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
24	Mr.Sushil Borkar	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
25	Mr.Purshottam chowke	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
26	Ms.Prachi Pendke	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
27	Ms.P.V.Upadhyay	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
28	Ms.Vaishali Asare	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017



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29	Ms.Meenal Pannase	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
30	Ms.Devshree Patrikar	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
31	Ms.R.Jichkar	2 week STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design", MHRD, IIT Kharagpur	26/12/2016 to 4/2/2017
32	Amit Kumar	Workshop on "Research Documentation Using Latex"	15/12/2016 - 17/12/2016
33	Prof.J.Gabhane	Workshop on "Research Documentation Using Latex"	15/12/2016 - 17/12/2016
34	Ms.Tejal Irkhede	Workshop on "Research Documentation Using Latex"	15/12/2016 - 17/12/2016
35	Ms.Prerna Deshmukh	Workshop on "Research Documentation Using Latex"	15/12/2016 - 17/12/2016
36	Ms. Shradha Thakare	Workshop on "Research Documentation Using Latex"	15/12/2016 - 17/12/2016
37	Mr.N.A.Ghodichor	Workshop on "Research Documentation Using Latex"	15/12/2016 - 17/12/2016
38	Mrs.P.S.Karemore	Workshop on "Research Documentation Using Latex"	15/12/2016 - 17/12/2016
39	Mr.P.Tembhare	Workshop on "Research Documentation Using Latex"	15/12/2016 - 17/12/2016
40	Prof.S.N.Agrawal	One Week STTP on DSC dsPIC33EP256MC202 & Its applications in Electrical Engineering	19/12/2016 -23/12/2016



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41	Prof.B.S.Dani	One Week STTP on DSCdsPIC33EP256MC202 & Its applications in Electrical Engineering	19/12/2016 -23/12/2016
42	Prof.Vishakha Zade	ISTE approved STTP on “ Advanced Electrical Drives and its Applications .	13/06/16 - 18/06/16
43	Prof.H.P.Thakre	ISTE approved STTP on “ Advanced Electrical Drives and its Applications .	13/06/16 - 18/06/16
44	Prof.A.R.Sonalikar	ISTE approved STTP on “ Advanced Electrical Drives and its Applications .	13/06/16 - 18/06/16
45	Prof.S.C.Suke	ISTE approved STTP on “ Advanced Electrical Drives and its Applications .	13/06/16 - 18/06/16
46	Prof.D.D.Dhawale	STTP on "Power Generation Technology For Sustainable Future"	17/04/2017-19/04/2017
47	V S Ghutke	One day All india Seminar on "Pile Foundations with special context to metro rail works" Organised by I E I (India), Nagpur	15/4/2017 to 15/4/2017
48	Prof.N.R.Hatwar	Workshop on "Research Documentation Using Latex" Organised by CT Dept. At PCE,Nagpur	15-17 Dec-2016
49	Prof.M.M.Gudhane	Workshop on "Research Documentation Using Latex"Organised by CT Dept. At PCE,Nagpur	15-17 December-2016
50	Prof. P. S. Bogawar	Workshop on "Research Documentation Using Latex"Organised by CT Dept. At PCE,Nagpur	15-17 December-2016
51	Prof. P. N Fale	Workshop on "Research Documentation Using Latex" Organised	15-17 December-2016



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		by CT Dept. At PCE,Nagpur	
52	Prof. B. M. Manjre	Workshop on "Research Documentation Using Latex" Organised by CT Dept. At PCE,Nagpur	15-17 December-2016
53	Ms. Priyanka B. Dongre	Workshop on "Research Documentation Using Latex" Organised by CT Dept. At PCE,Nagpur	15-17 December-2016
54	Dr. N. S. Ugemuge	Two-week ISTE STTP on "CMOS, Mixed signal and Radio frequency VLSI design"	26/12/2016 -04/02/2017
55	Mrs. A.A.Kulkarni	Four Week AICTE approved FDP by IIT Bombay on "Use of ICT in Education for Online and Blended Learning"	02-05-16 to 10-07-16
56	Mr. R. N. Pakade	Four Week AICTE approved FDP by IIT Bombay on "Use of ICT in Education for Online and Blended Learning"	02-05-16 to 10-07-16
57	Mrs.K.M.Bogawar	AICTE -ISTE approved One week STTP on "Advanced VLSI Design Using Cadence Tools" YCCE,Nagpur	2/04/2017-7/04/2018
58	Dr.S.S.Shriramwar	Two-week ISTE STTP on "CMOS, Mixed signal and Radio frequency VLSI design"	30/01/2017 -04/02/2017
59	Dr.P.R.Rothe	One-week STTP on Outcome Base Approach to Teaching ,Learning & Assessment in Engg for Accrediation,PIET,Nagpur	13/12/2016 -17/12/2016
60	Mrs.M.V.Vyawahare	Two-week ISTE STTP on "CMOS, Mixed signal and Radio frequency	30/01/2017 -04/02/2019



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		VLSI design”	
61	Mr.C.N.Bhoyar	Two-week ISTE STTP on “CMOS, Mixed signal and Radio frequency VLSI design”	30/01/2017 -04/02/2020
62	Mrs.A.P.Khandait	Two-week ISTE STTP on “CMOS, Mixed signal and Radio frequency VLSI design”	30/01/2017 -04/02/2021
		One Week ISTE STTP on DCS Switching & Routing,PBCOE,Nagpur	19 /12/2016 – 23/12/ 2016
63	Mrs.P.J.Suryawanshi	Two-week ISTE STTP on “CMOS, Mixed signal and Radio frequency VLSI design”	30/01/2017 -04/02/2022
64	Ms.Divya Meshram	One-week ISTE approved One week STTP on “VLSI Design Using EDA tools and CMOS Implementation”PCE,Nagpur	19/6/0217 -24/06/2017
65	Ms.S.G.Mungale	One-week ISTE approved One week STTP on “VLSI Design Using EDA tools and CMOS Implementation”PCE,Nagpur	19/6/0217 -24/06/2017
66	Mrs. A.S.Khobragade	One Week ISTE STTP on FPGA Based Digital Systems Design ,SRKNEC,Nagpur	21/11/2016 - 26/11/2016
67	Mr.R.C.Iyer	One Week ISTE STTP on FPGA Based Digital systems Design ,SRKNEC,Nagpur	21/11/2016 - 26/11/2016
68	Mr.A.Zade	Two-week ISTE STTP on “CMOS, Mixed signal and Radio frequency VLSI design”	30/01/2017 -04/02/2018
69	Ms.Deepali Sukhdeve	Two-week ISTE STTP on “CMOS, Mixed signal and Radio frequency	30/01/2017 -04/02/2029



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		VLSI design”	
70	Ms Anupa Kavale	Two-week ISTE STTP on “CMOS, Mixed signal and Radio frequency VLSI design”	30/01/2017 -04/02/2031
		One Week ISTE STTP on DCS Switching & Routing,PBCOE,Nagpur	19 /12/2016 – 23/12/ 2016
71	Ms.Deepali Giri	Two-week ISTE STTP on “CMOS, Mixed signal and Radio frequency VLSI design”	30/01/2017 -04/02/2032
72	Ms.Rohini Gulhane	Two-week ISTE STTP on “CMOS, Mixed signal and Radio frequency VLSI design”	30/01/2017 -04/02/2033
73	Mr.Upesh Patel	Two-week ISTE STTP on “CMOS, Mixed signal and Radio frequency VLSI design”	30/01/2017 -04/02/2034
74	Ms.Shweta Pethe	Two-week ISTE STTP on “CMOS, Mixed signal and Radio frequency VLSI design”	30/01/2017 -04/02/2035
75	Ms.Priyanka Chopkar	One Week ISTE STTP on FPGA Based Digital Systems Design ,SRKNEC,Nagpur	21/11/2016 - 26/11/2016
76	Mr. Manoj Mahore	Two Days Workshop on “Basics of MATLAB and its Applications for Engineering Problems” by Mech Dept.	18/01/2017 – 19/01/2017



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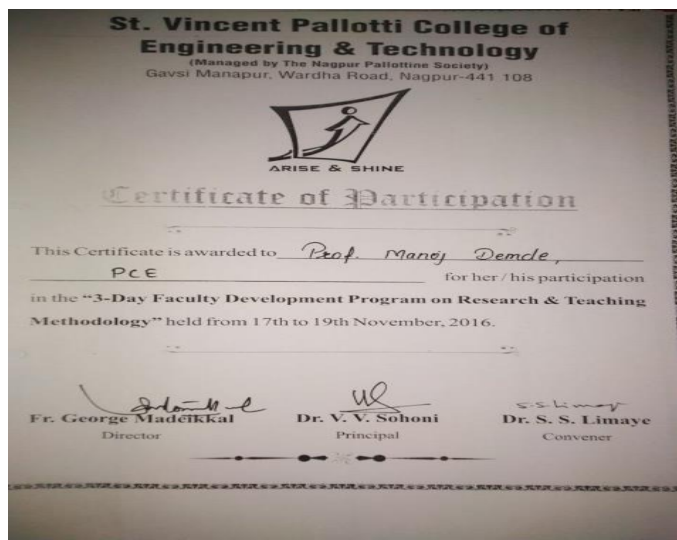
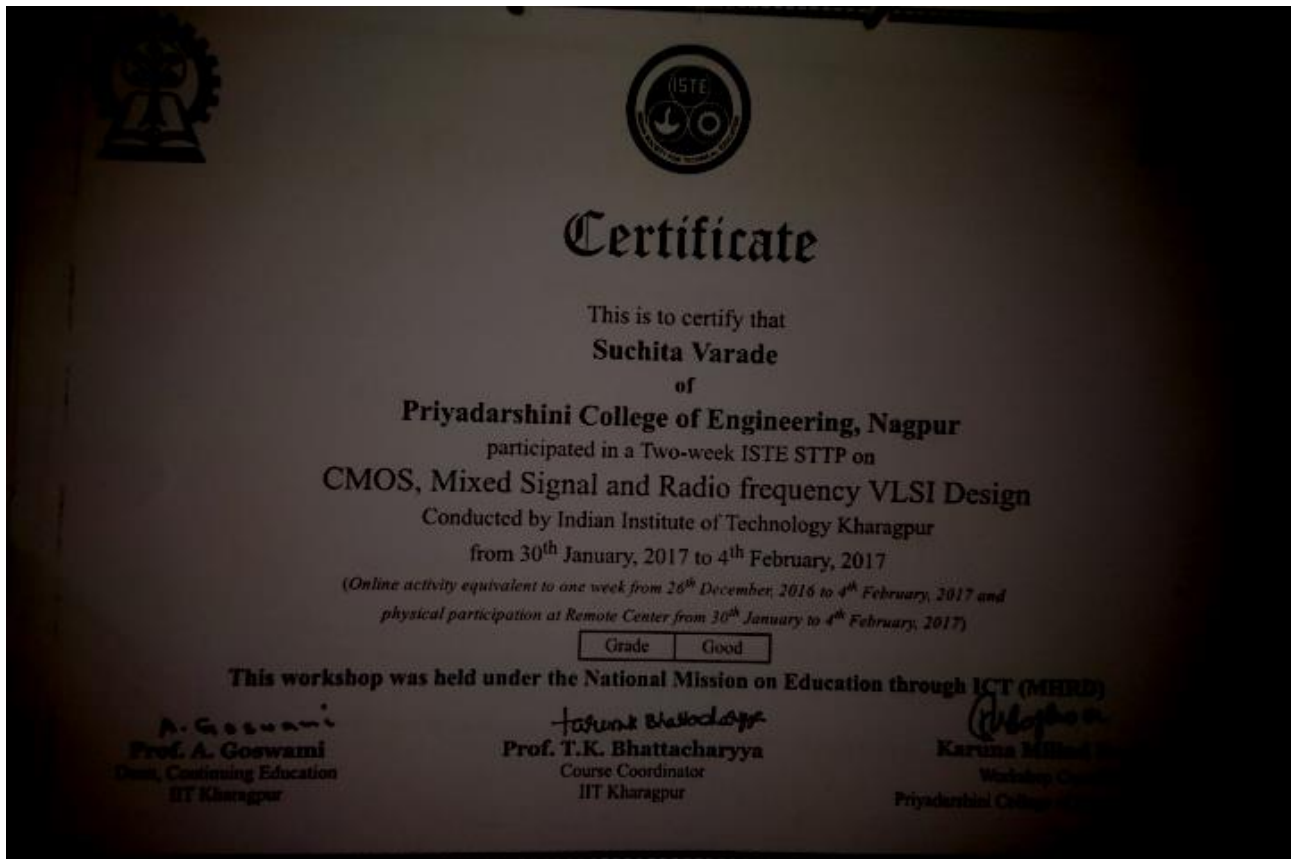
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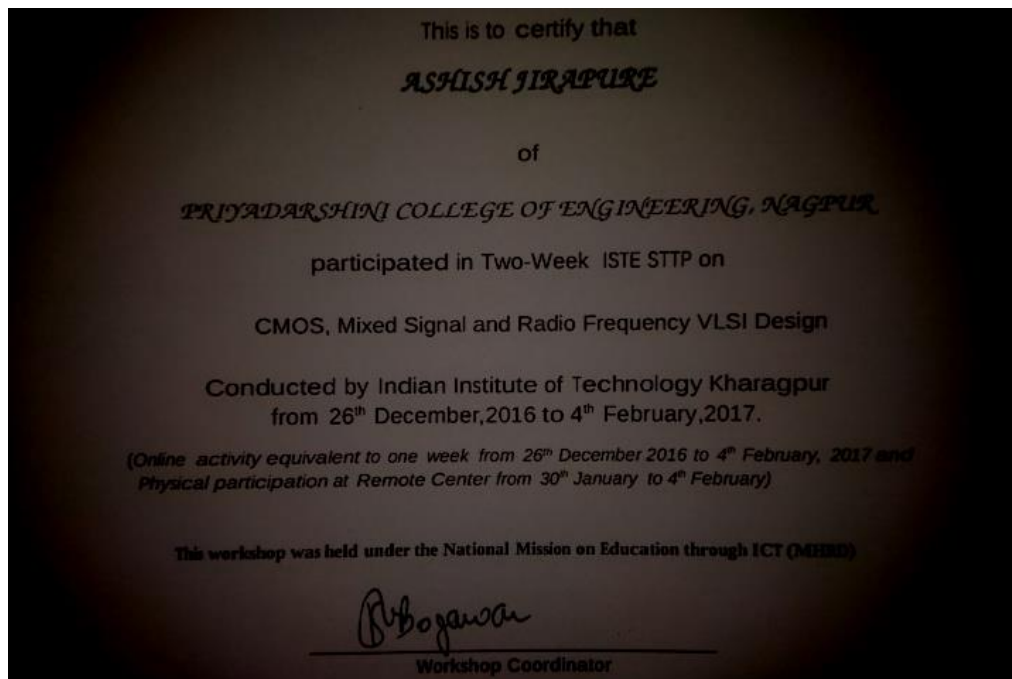
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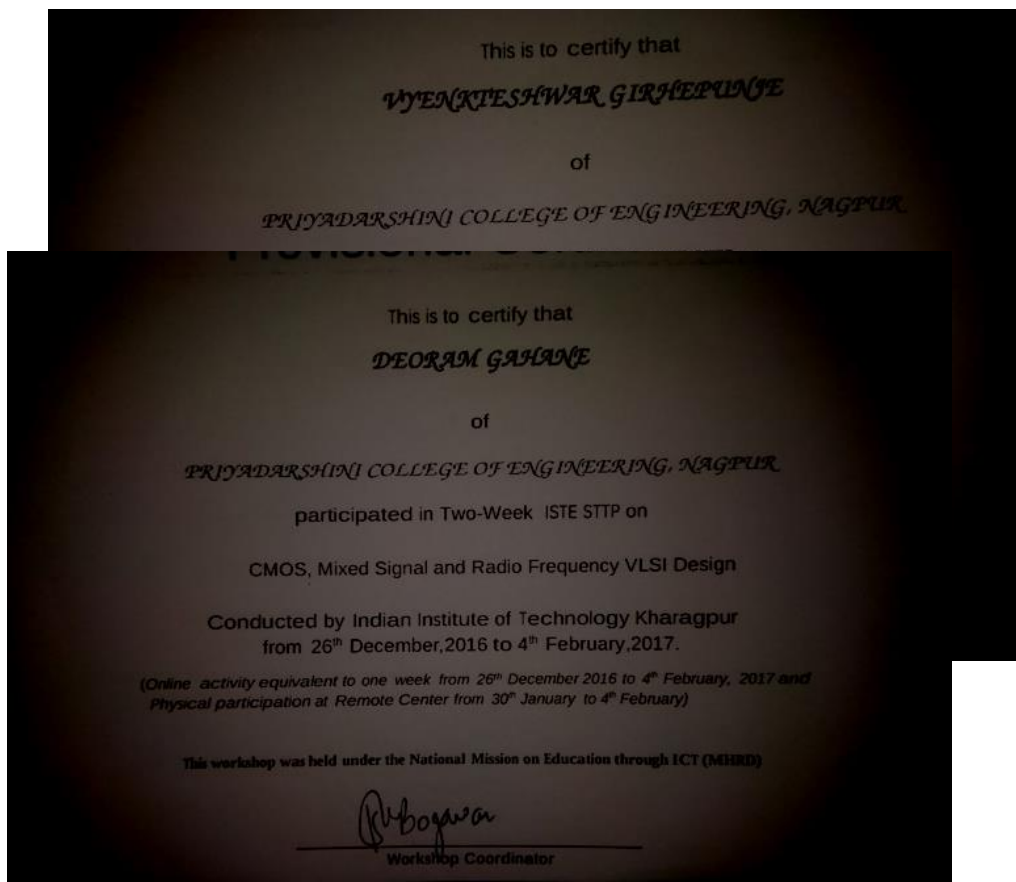
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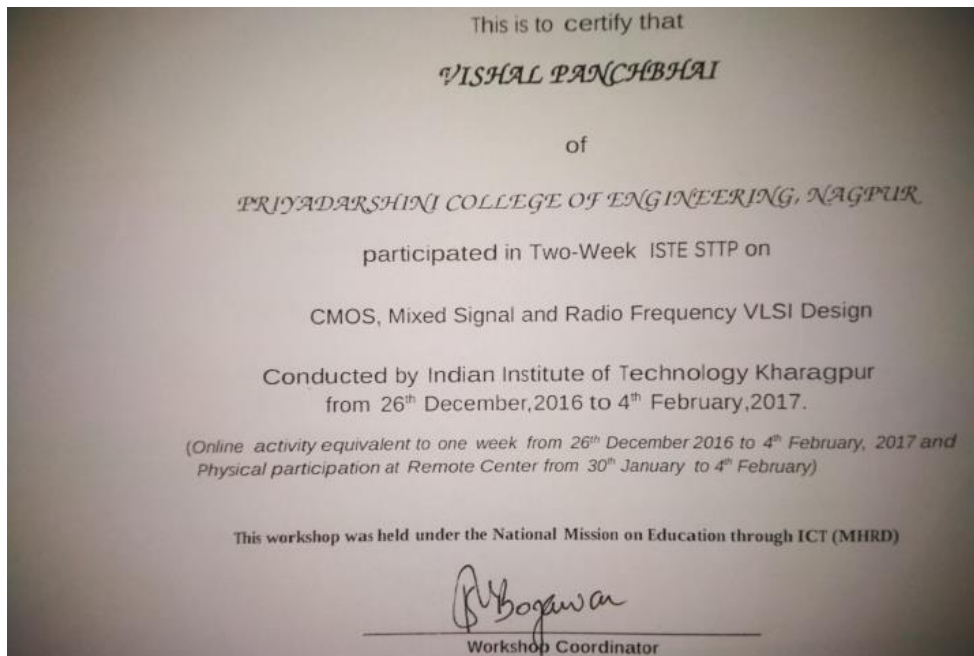
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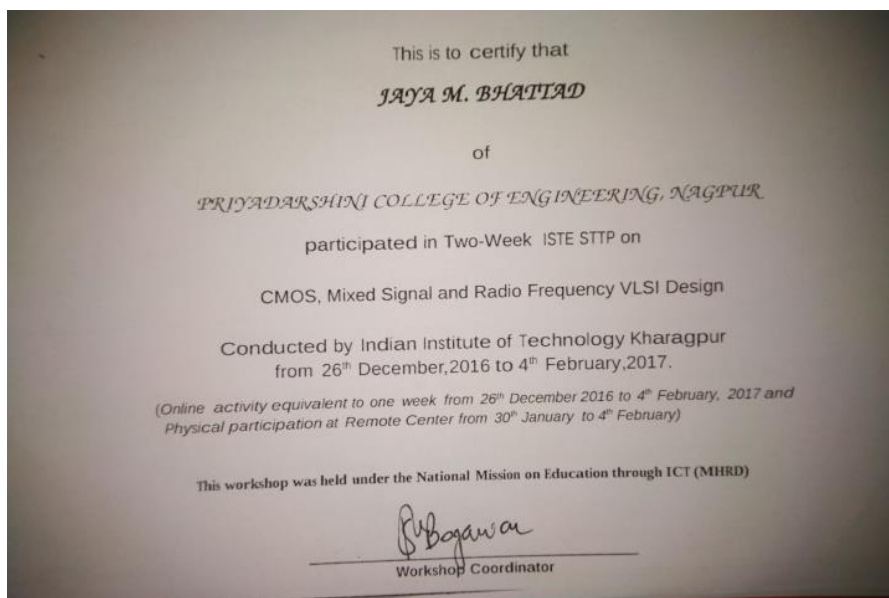
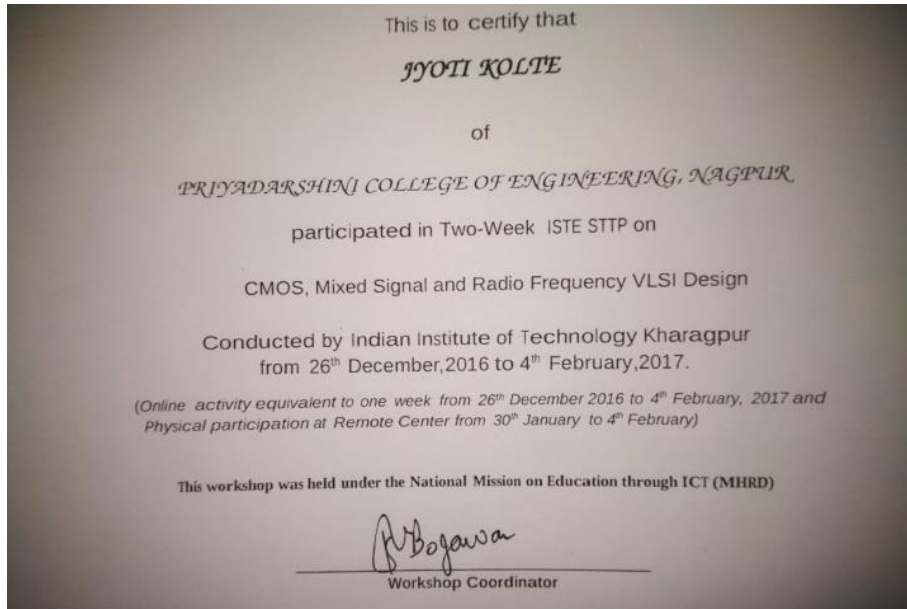
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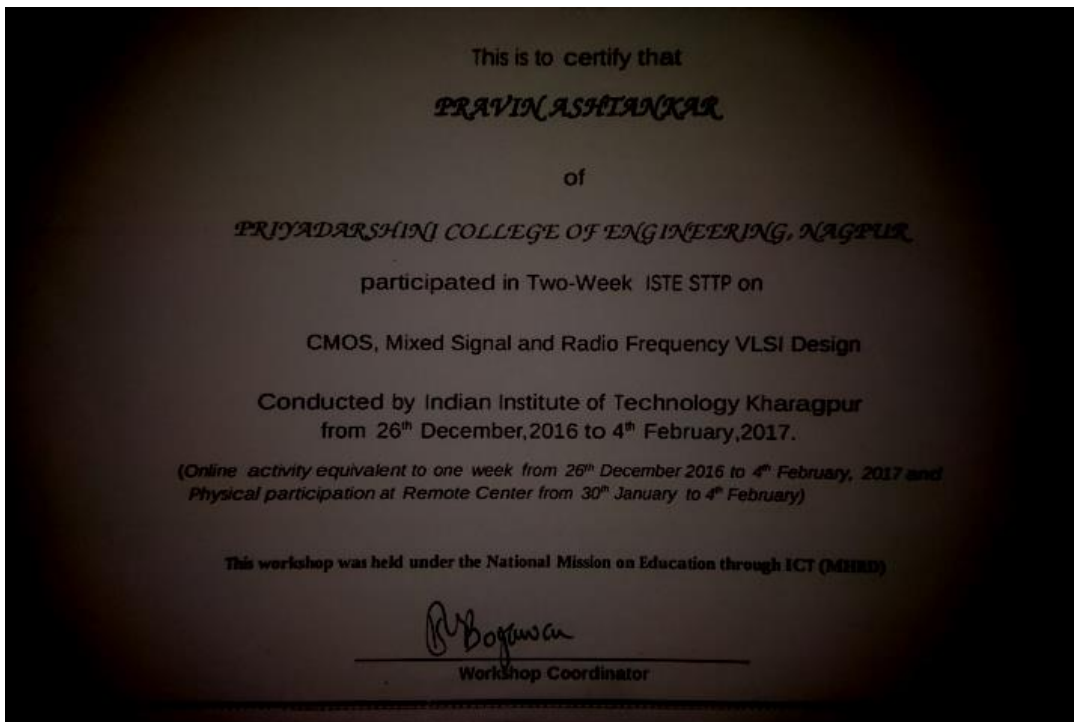
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This is to certify that

VRUSHALI NASRE

of

PRIYADARSHINI COLLEGE OF ENGINEERING, NAGPUR

participated in Two-Week ISTE STTP on

CMOS, Mixed Signal and Radio Frequency VLSI Design

Conducted by Indian Institute of Technology Kharagpur
from 26th December, 2016 to 4th February, 2017.

*(Online activity equivalent to one week from 26th December 2016 to 4th February, 2017 and
Physical participation at Remote Center from 30th January to 4th February)*

This workshop was held under the National Mission on Education through ICT (NME-ICT)

Workshop Coordinator



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This is to certify that

SHEETAL MUNGALE

of

PRIYADARSHINI COLLEGE OF ENGINEERING, NAGPUR

participated in Two-Week ISTE STTP on

CMOS, Mixed Signal and Radio Frequency VLSI Design

Conducted by Indian Institute of Technology Kharagpur
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This is to certify that

NILIMA BODHAYE

of

PRIYADARSHINI COLLEGE OF ENGINEERING, NAGPUR

participated in Two-Week ISTE STTP on

CMOS, Mixed Signal and Radio Frequency VLSI Design

Conducted by Indian Institute of Technology Kharagpur
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This is to certify that

NEHA SONDHIA

of

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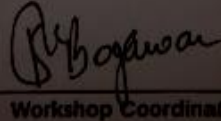
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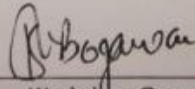
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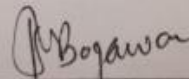
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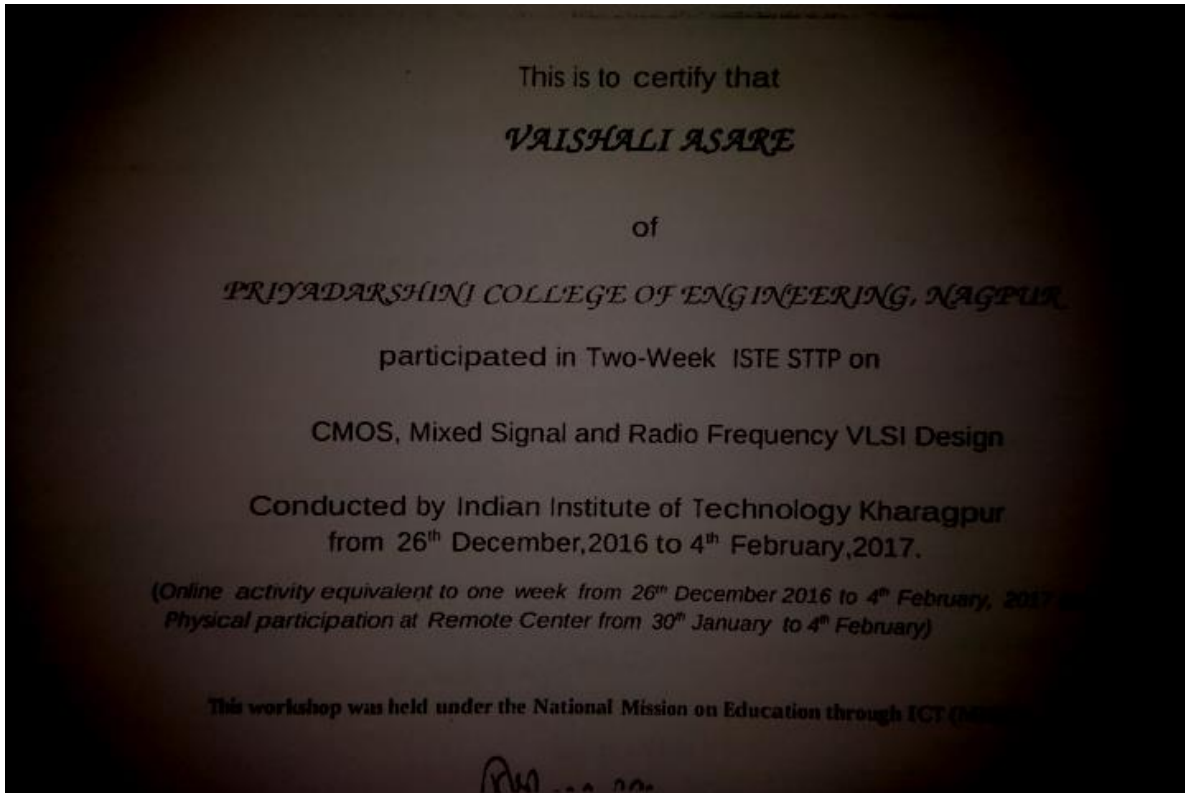
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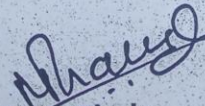


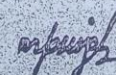
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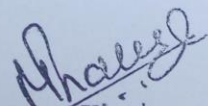


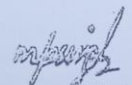
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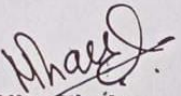


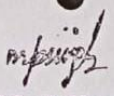
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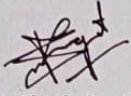
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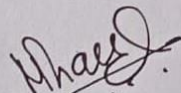



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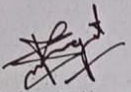
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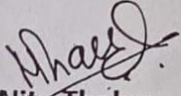
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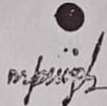


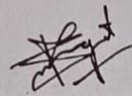
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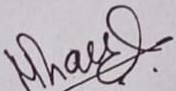
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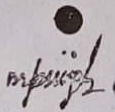


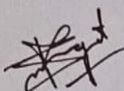
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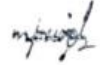


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Department of Electrical Engineering


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This is to certify that Mr/Ms/Mrs VISHAKHA RADE
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participated in one week ISTE approved Short Term Training Program on "Advanced Electrical Drives and its Applications" organized by Department of Electrical Engineering, Priyadarshini College of Engineering, Nagpur during 13th June 2016 – 18th June 2016.

(16-12)

 Mr. C.S. Kamble Co-ordinator	 Mrs. P.S. Gotekar Co-ordinator	 Dr. (Mrs) S.P. Muley HoD, Electrical Engg.	 Dr. M.P. Singh Principal
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 Mr. C.S. Kamble Co-ordinator	 Mrs. P.S. Gotekar Co-ordinator	 Dr. (Mrs) S.P. Muley HoD, Electrical Engg.	 Dr. M.P. Singh Principal
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Co-ordinator


Mrs. P.S. Gotekar
Co-ordinator


Dr. (Mrs) S.P. Muley
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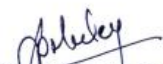

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


OF PARTICIPATION


We, the undersigned do hereby proudly present this certificate to
Prof./Dr. D. D. Dhawale of PCE, Nagpur
for actively participating in **Short Term Training Program**
on "**Power Generation Technologies for Sustainable Future**" organized by
Department of Electrical Engineering in association with **The Institution of**
Engineers (I) Nagpur local Centre, Nagpur during **17th - 19th April 2017**.

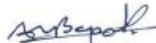
We wish him/her best wishes for his/her future endeavors


Prof. A.N. Deshmukh
Co-ordinator


Dr. Dilip Mase
Hon'ble Chairman,
IE(I),NLC, Nagpur


Prof. P.D. Debre
Convener & Head EE


Mr. Milind Pathak
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HOD CT


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
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Provisional Certificate

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NILESH UGEMUGE

of

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participated in Two-Week ISTE STTP on

CMOS, Mixed Signal and Radio Frequency VLSI Design

Conducted by Indian Institute of Technology Kharagpur
from 26th December, 2016 to 4th February, 2017.

(Online activity equivalent to one week from 26th December 2016 to 4th February, 2017 and
Physical participation at Remote Center from 30th January to 4th February)

This workshop was held under the National Mission on Education through ICT (MHRD)

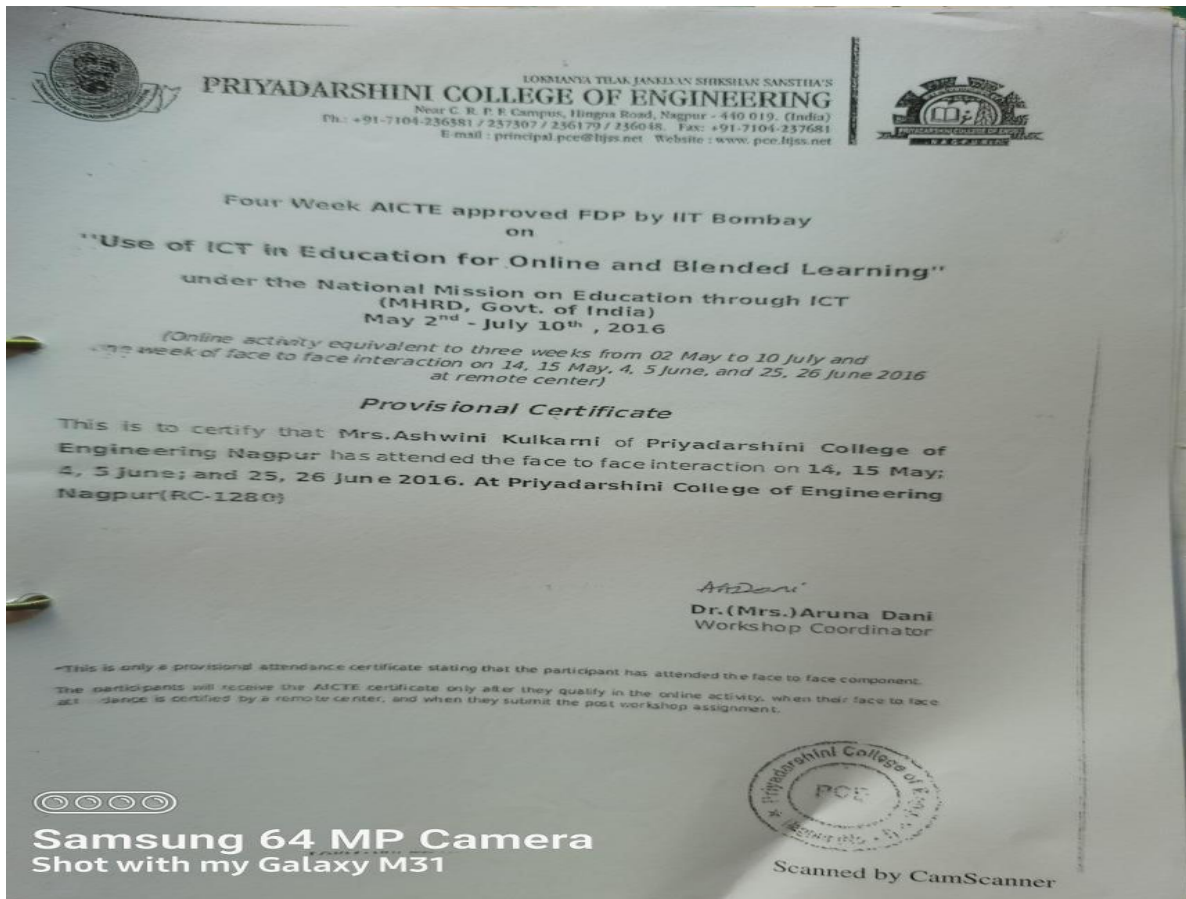
Workshop Coordinator



Samsung 64 MP Camera



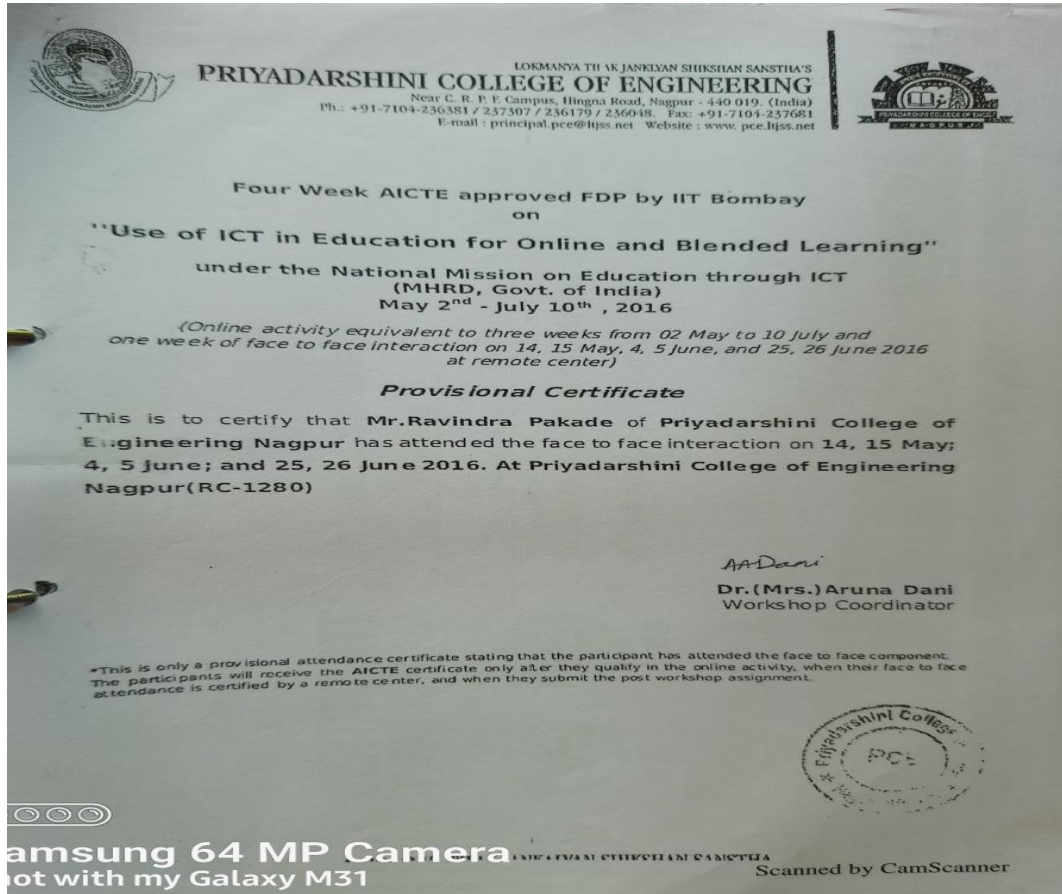
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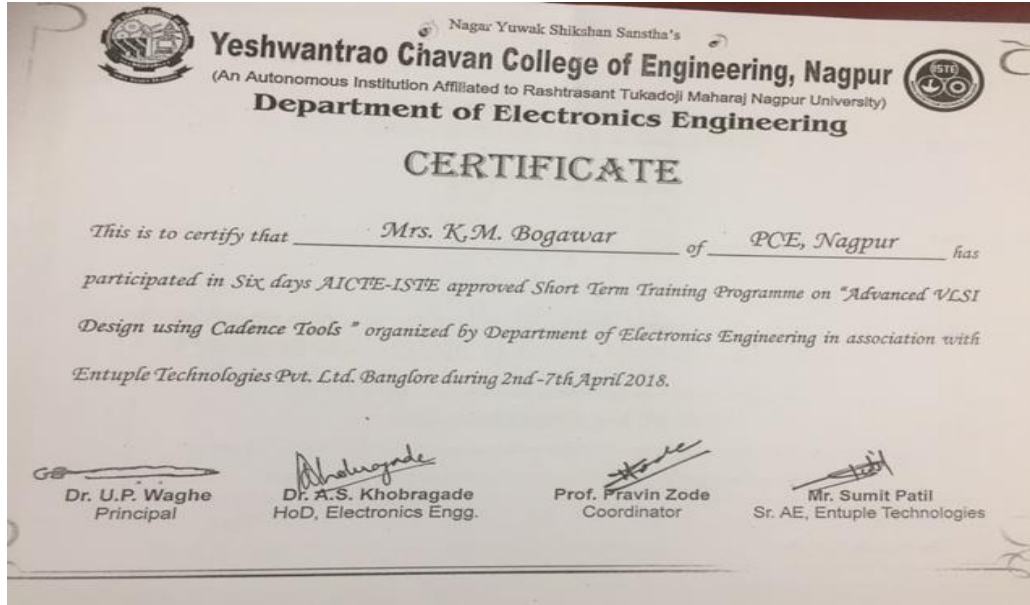
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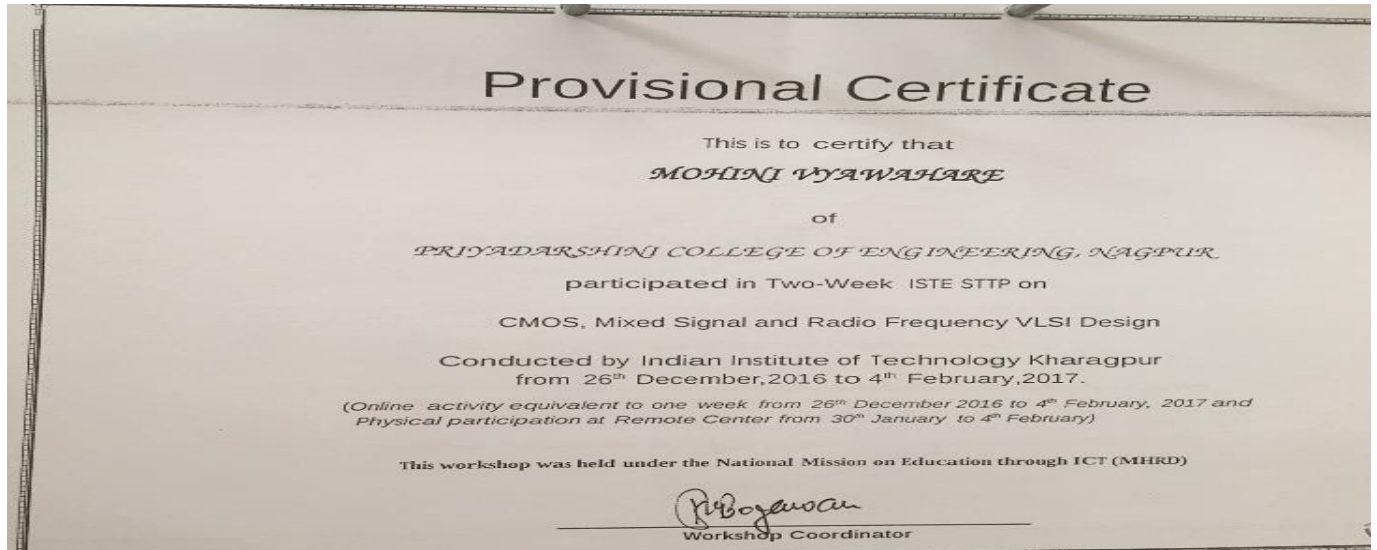
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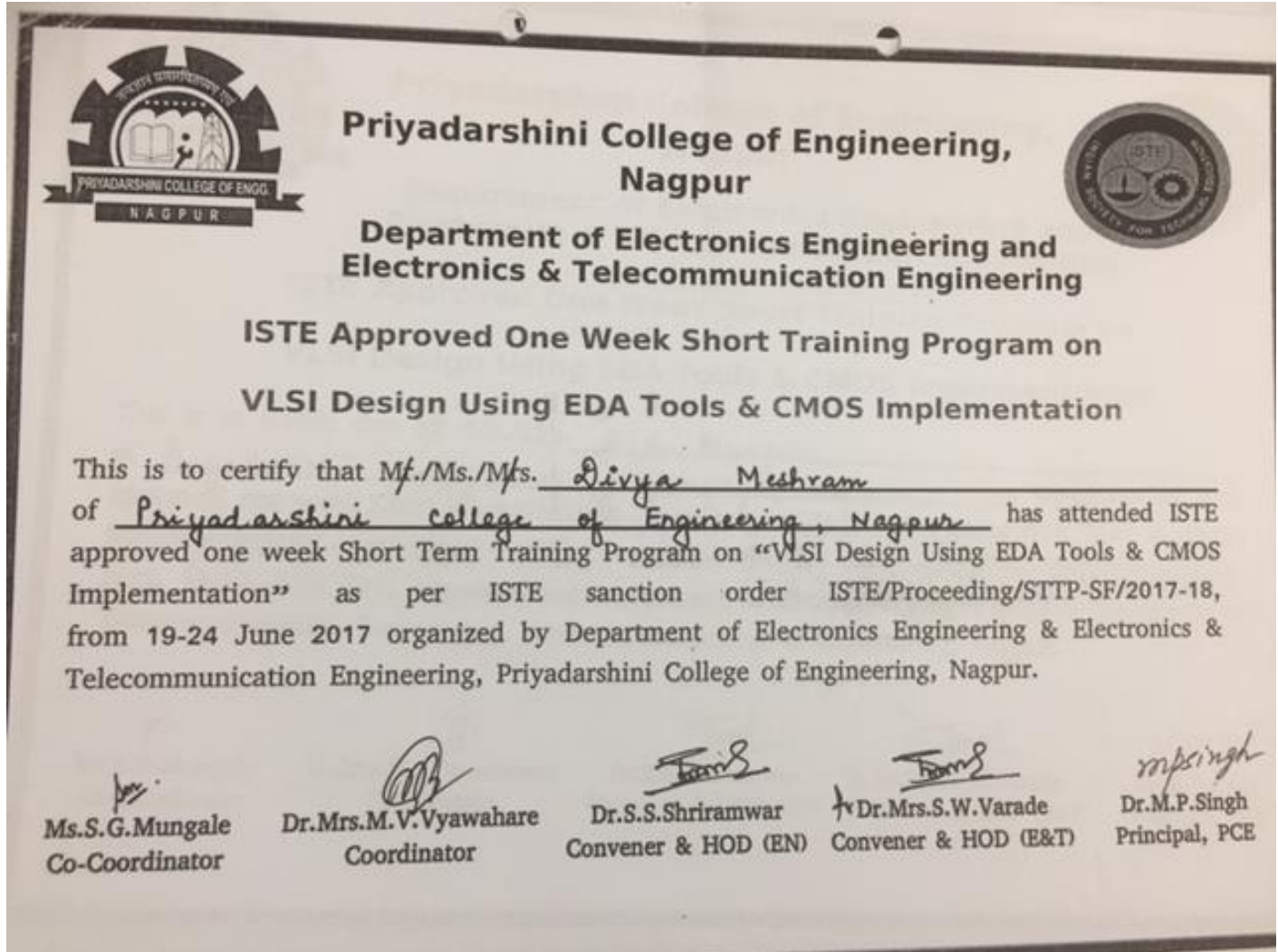
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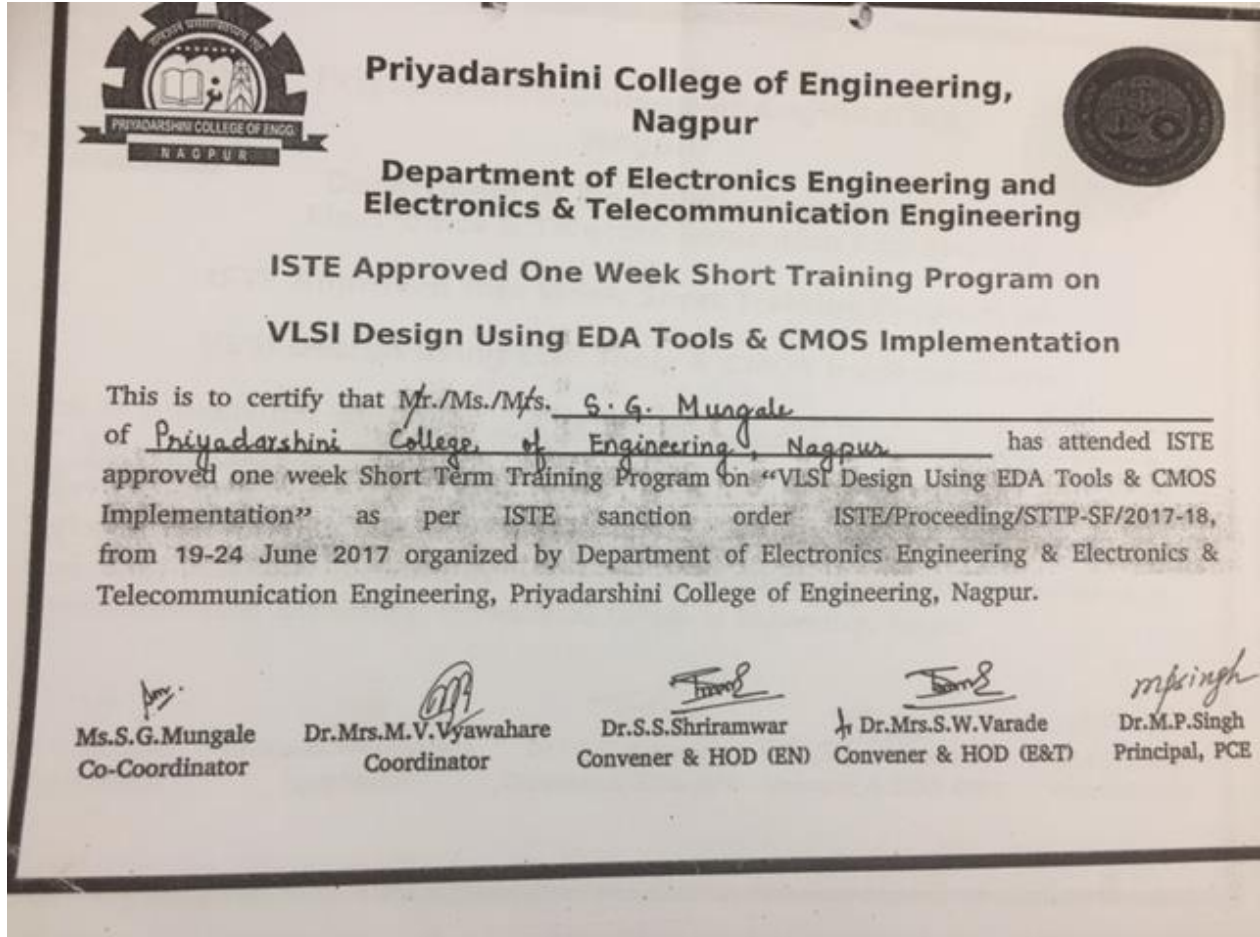
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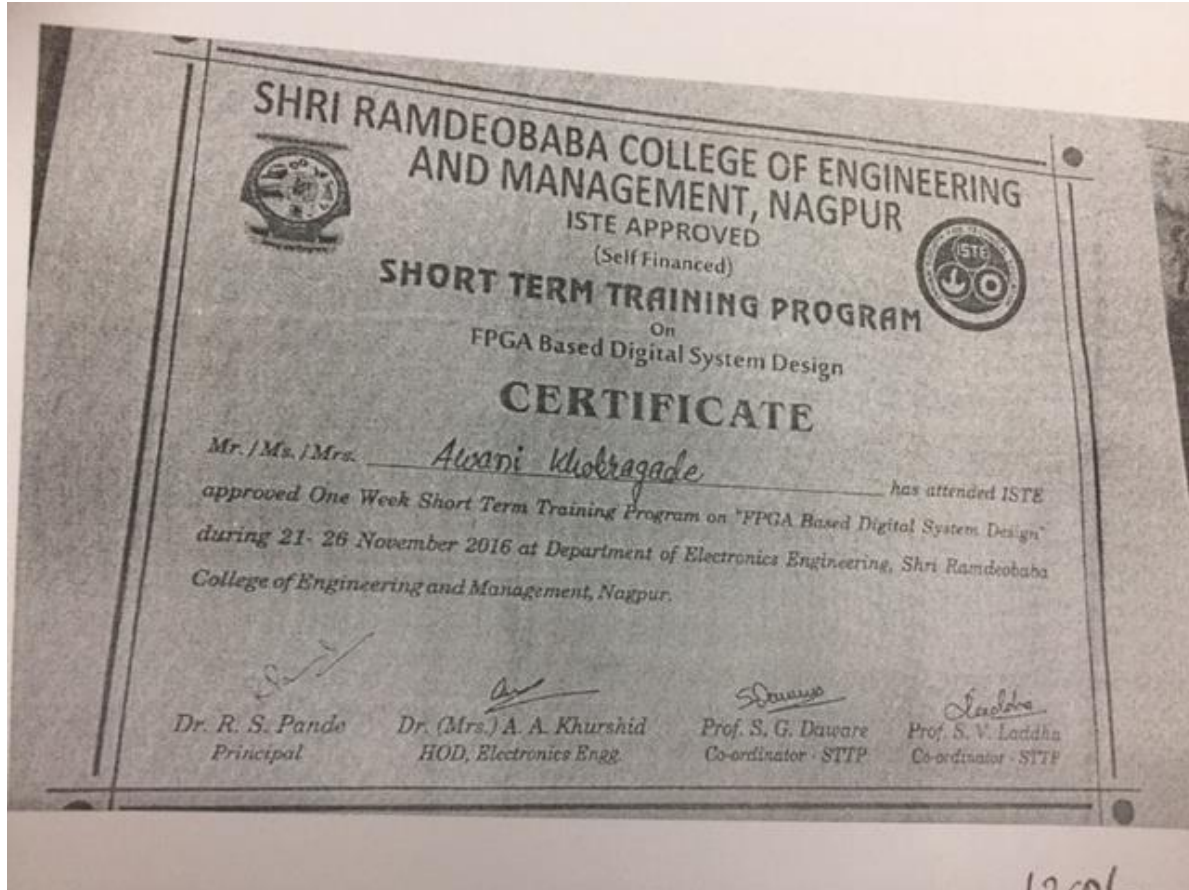
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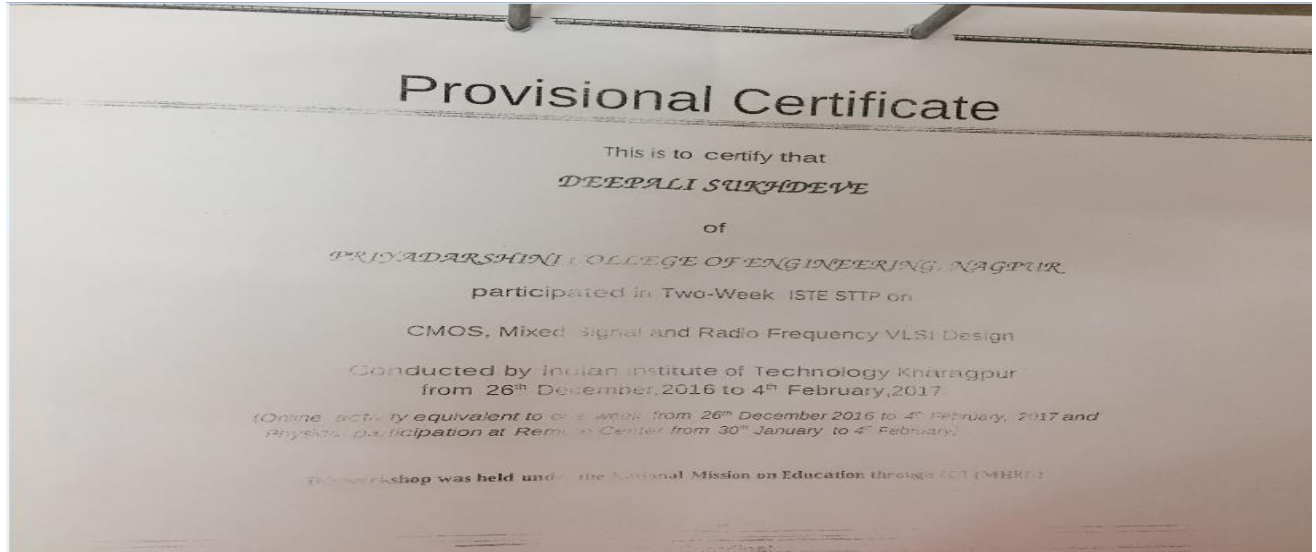


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