Lokmanya Tilak Jankalyan Shikshan Sanstha's

Priyadarshini College of Engineering

Priyadarshini Campus, Hingna Road Nagpur-440019

www.pcenagpur.edu.in

Affiliated to RTM Nagpur University

Internal Quality Assurance Cell

AICTE Sponsored

Online Short Term Training Program (STTP)

On

Recent Trends & Applications in Analog VLSI Design

26th to 31st October 2020

Report on STTP

AICTE sponsored

Online One Week Short Term Training Program on Recent Trends & Applications in Analog VLSI Design

Day-1

Date: 26/10/2020

Program was inaugurated by Dr. R. M. Patrikar Professor VNIT,Nagpur, Dr. M.P.Singh, Principal & Dr.S.A.Dhale ,Vice-Principal, PCE

Program started with introduction and welcome of guests by Mrs.K.M.Bogawar, Coordinator STTP

Dr.S.S.Shriramwar, Convener & IQAC coordinator briefed about the objectives of STTP.

Dr.S.A.Dhale,Vice Principal spoke about the benefits of STTP to the participants.

Dr. R.M. Patrikar, Chief guest, in his speech focused on Recent Trends in VLSI Design.

Dr. M. P. Singh, Principal * briefed about the achievements of PCE , extended best wishes and Congratulated the STTP team for their efforts.

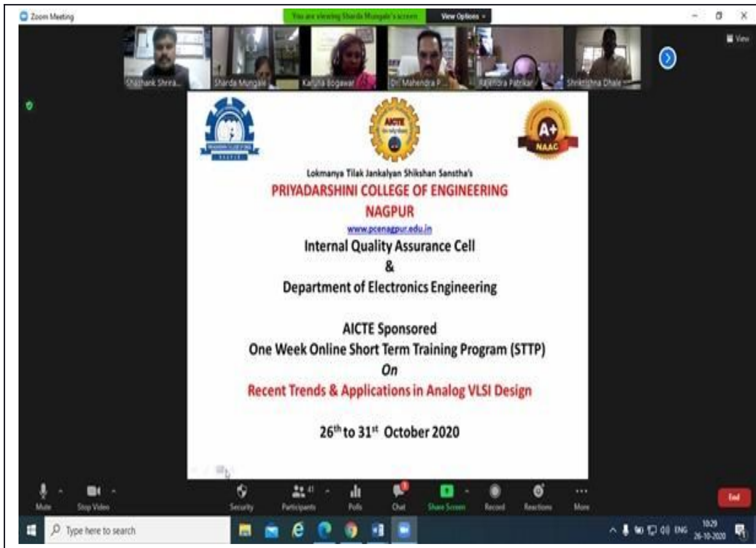
Vote of thanks was proposed by Ms.S.G.Mungale, Coordinator STTP

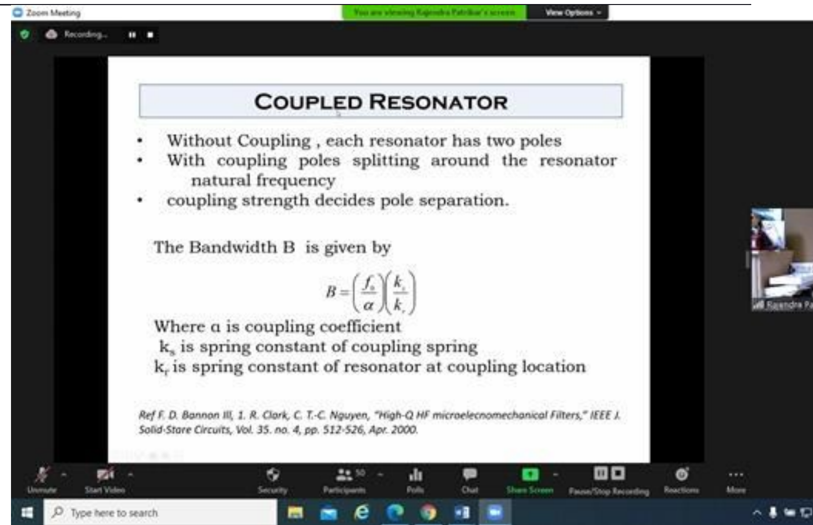
Two sessions were delivered today .

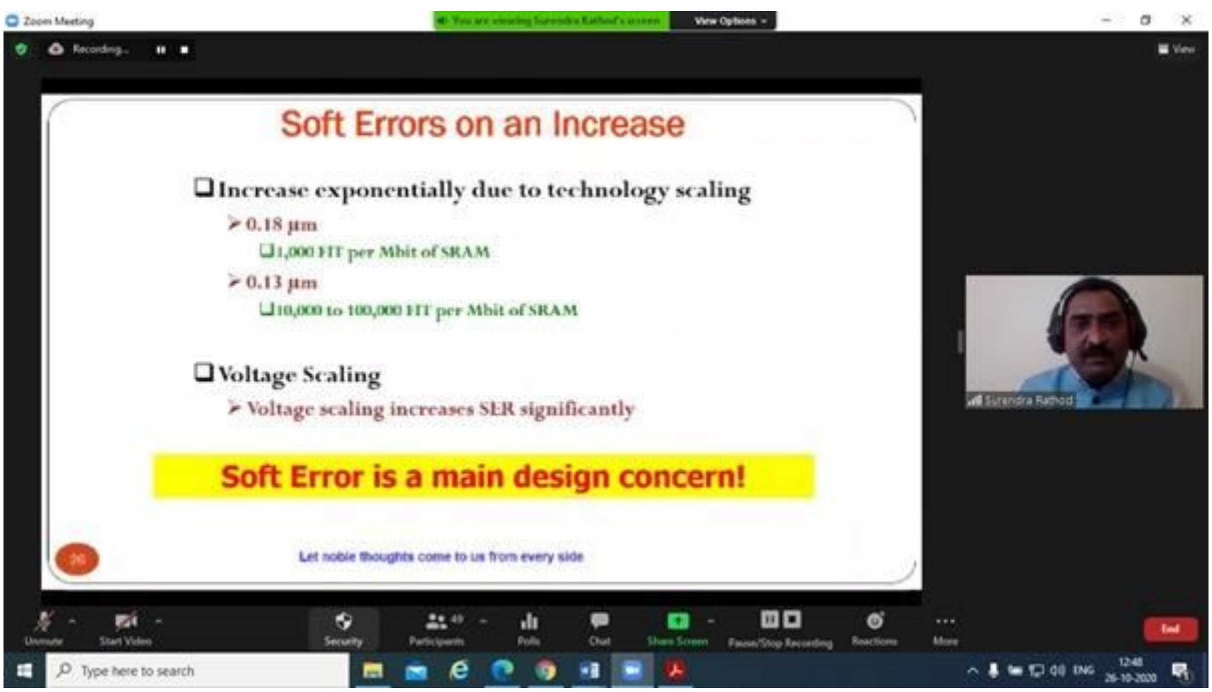
Session-1: Dr. R. M. Patrikar Professor VNIT,Nagpur delivered expert talk on " Analog Design using MEMS".

Session-2: Dr. Surendra Rathod Professor, Sardar Patel Institute of Technolgy Mumbai delivered expert talk on " Evolution of Microelectronics & its future trends with respect to Nanometer era."

Total 104 participants were selected for the STTP on first come first serve basis





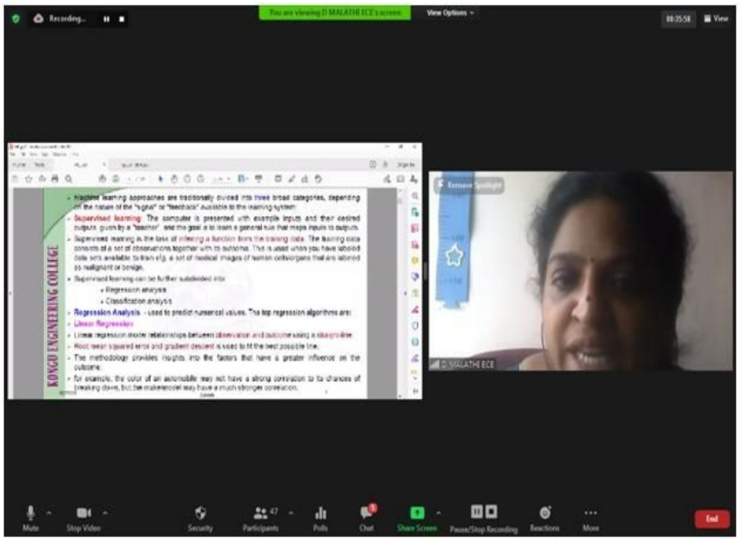


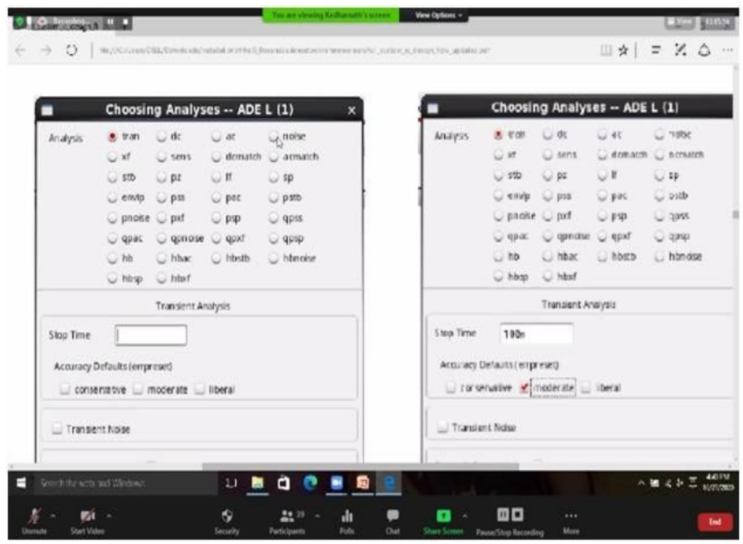
Day 2

Date: 27/10/20

Session-1: Dr. D. Malathi, Professor(ECE) Kongu College of Engineering, Erode, Tamilnadu delivered expert talk on " Machine learning in VLSI".

Session-2 & 3 : Mr. L. V. Kedharnath, field application Engineer Entuple Technology Pvt. Ltd. delivered expert talk in Full custom IC design using Cadence



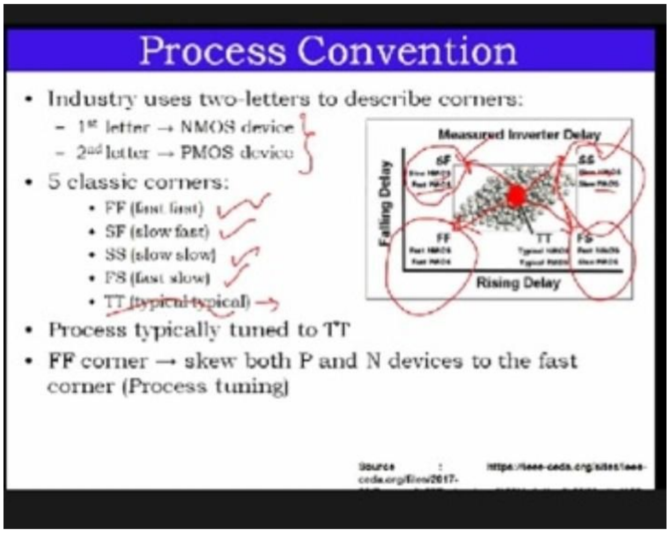


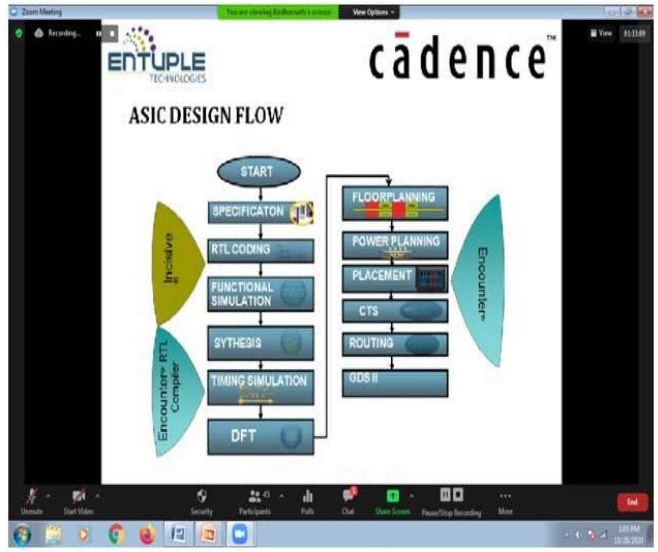
Day 3

Date: **28/10/2020**

Session-1: Dr. Prachi Palsodkar from YCCE, Nagpur delivered expert talk on " Checkouts in Analog VLSI"

Session-2 & 3 : Mr. L. V. Kedharnath, field application Engineer Entuple Technology Pvt. Ltd. delivered expert talk in Semi custom IC design using Cadence.



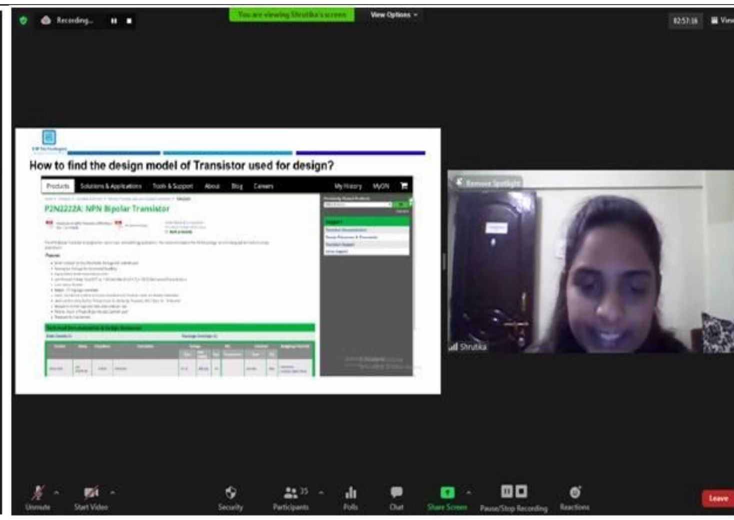


Day 4

Date: 29/10/20

Session-1, 2 : Dr. Pravin Zode from YCCE, Nagpur delivered expert talk & Hands on session on Analog VLSI Design using SPICE.

Session- 3 : Ms. Shrutika Solat, RF Design Engineer from SM Technologies Pvt. Ltd. delivered expert talk on Introduction & Application of voltage Amplifier IC & Design using EDA Tools.



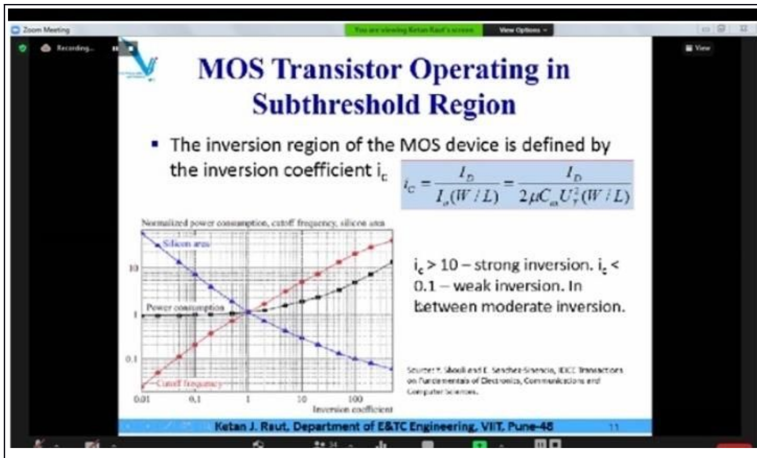
Day 5

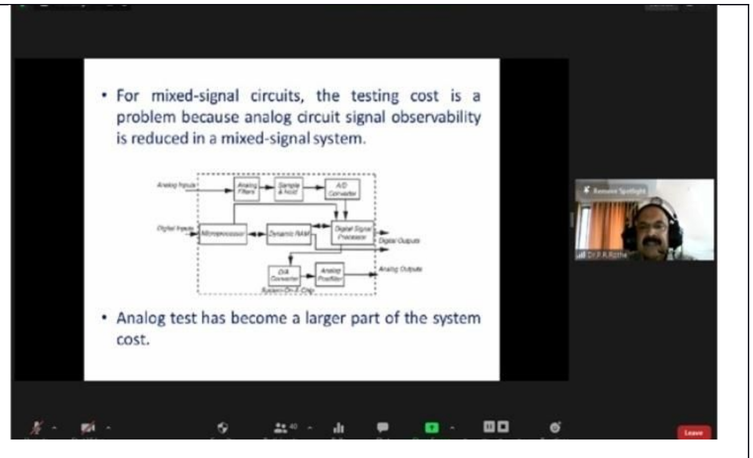
Date: 30/10/2020

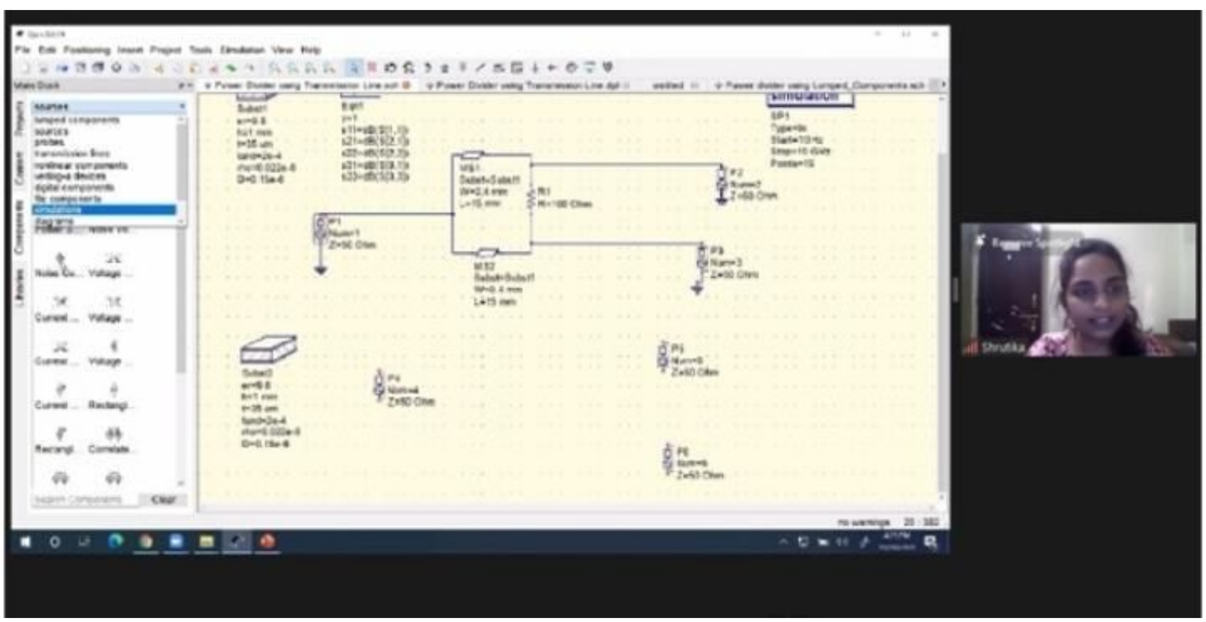
Session-1 : Dr. Ketan Raut from Vishwakarma Institute of Information Technology delivered expert on Recent Trends in Low Power VLSI Design.

Session 2: Dr. P. R. Rothe from Priyadarshini College of Engineering, Nagpur delivered expert talk on DSP Based Analog & Mixed Signal Testing.

Session- 3 : Ms. Shrutika Solat, RF Design Engineer from SM Technologies Pvt. Ltd. delivered expert talk on Introduction & Application of Power divider IC Design







Day 6

Date: 31/10/20

Session 1- Mr.Vaibhav Pavnaskar , Design Engineer Microchip, Ireland delivered expert talk on Recent Trends in Analog RF IC Design .

Session-2: Dr. D. Malathi, Prof. Kongu Engg.College , Erode, TN delivered expert talk on " VLSI Signal Processing".

The STTP ended with the valedictory function in presence of Dr.M.P.Singh, Principal,PCE,Nagpur.

Total 18 sessions were delivered by 11 experts from Academia and Industry and one International expert from Ireland.

Feedback on STTP was taken from the participants and received very good feedback.

Summary Report of STTP was presented by Ms.S.G.Mungale

Mrs. K.M.Bogawar proposed vote of thanks

Dr. M. P. Singh Principal Congratulated the the organizing team for successful completion of STTP.